

IN THE SPECIFICATION

Please amend the specification as follows:

Page 2, paragraph 4:

Figure 2 shows an embodiment of a processing light modulation removal unit according to the invention.

Page 2, paragraph 6:

Figure 2 shows in more detail block schematic a light modulation removal unit LMRU2 according to the invention. At a video input VI the light modulation removal unit receives the YCrCb signals from the processing unit PU2-PU1 (see Fig. 1). The light modulation removal unit LMRU2 comprises different parts. A first part having in this example five field memories FM1-FM5 for storing different fields of the received signal. A second part comprising of (in this example) three de-interlacers DIL1-DIL3. Further a part having three motion detectors MD1-MD3. Further a part having a modulation estimator ME and a part having an averager A. After the different fields are stored in the field memories FM1-FM5 the fields are supplied to three de-interlacers whereby the first de-interlacer DIL1 receives at a first input the input signals after filtered in a band split filter BF1 and at a second input the output signals of FM3. The second de-interlacer DIL2 receives at a first input the output signals of FM1 after filtering in a second band split filter BF2 and at a second input receives the output signal of a field memory FM4. The third de-interlacer DIL3 receives at the first input signals at output of field memory FM2 and at the second input the output signals after field memory FM5. The output of the de-interlacers DIL1-DIL3 is supplied as a first input to respectively motion detector MD1-MD3. At the second input the motion detector MD1 receives the output signal of field memory FM3. At a second input the motion detector MD2 receives the output signal of FM4 and the second input of motion detector MD3 receives the output signal of field memory FM5. The outputs of the motion detectors MD1-MD3 are supplied to a maximizer MX which supplies at the output the maximum of the three input signals. This output signal is supplied

via an amplifier AMP₁ to a subtractor at the negative input. The amplifier AMP₁ receives at a control input a motion sensitive signal MSS.